Company Name : Ablaze Wireless Inc.

Company Background :
Ablaze Wireless is founded in 2008 with the goal to provide highly secured, cost effective solution to the femtocell market. Over the course of the past few years, the company has achieved the impossible by delivering quality product with innovation while staying lean. In 2009, Ablaze Wireless expanded its operation by setting up an office in Taipei, Taiwan to work closely with the supply chain and the manufacturers to bring down the cost of the femto access point.

Our team has immense knowledge in mobile communications, digital signal processing, interference suppression, MIMO, and network optimization. With determination and the relentless pursue for perfection, the team announced the availability of Ablaze's first femtocell baseband processor in May 2011. It is our believe that there are tremendous opportunities awaiting in the femtocell market as long as the company can grow with innovation, superb quality, optimal cost, best service, and strong support.

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| Digital IC Design Engineer | The ideal candidate should be familiar with the ASIC front-end design process (and the related EDA tools), including micro-architecture, RTL coding, verification, FPGA bring-up, synthesis and static timing analysis. | Education Requirement: BS or MS degree in EE or related field. | Years of Experience : 3 years+
Experience in any of the following areas is desired (but not required):
• High-speed SOC design, such as embedded microprocessor, cache, DMA, on-chip interconnect / arbiter (AXI, AHB, Avalon, or OCP), or memory controller;
• Mobile communication, such as GSM/EDGE, CDMA2000, UMTS, WiMax or LTE;
• Digital signal processing, such as FFT, block transform, FIR filter;
• Authentication / cryptography, such as IPSec
Experience in any of the following areas is a plus:
• Hardware C model development or device driver development |
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<td>Communications Software Engineer</td>
<td>This position will be responsible for developing and porting protocol stacks for 3G mobile communication facilities. Ideal candidate will have experience in 2G/3G mobile communication device development.</td>
<td>Education Requirement: BS or MS degree in CS, EE or related fields.</td>
<td>The candidate is expected to be fluent in Verilog and C/C++. Familiarity with PERL or Matlab is a plus.</td>
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<td>Baseband Algorithm Engineer</td>
<td>We are looking for highly-innovative, dynamic, self-driven, result-oriented individuals with strong digital signal processing background for mobile communication systems such as GSM/EDGE, CDMA2000, UMTS, WiMAX and/or LTE.</td>
<td>Education Requirement: MS degree in EE related fields or Communication Science related</td>
<td>Work Experience : Over 3 years of work experience in designing, implementing, and verifying digital baseband receiver algorithms both in floating point and fixed point using Matlab or C/C++. Thorough understanding of at least one of the following digital communication areas is required: * Linear/non-linear equalization * RAKE * Channel estimation * Frequency and timing synchronization * Coding theory (e.g. turbo code, convolutional code, Reed Muller code) Also required is the ability to write algorithm design</td>
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| Communications Systems Engineer         | 1. Communication system algorithm and architecture design.                      | Education Requirement: MS degree in EE related fields or Communication Science related | Technical Skills:  
- Fluent in both Matlab and C programming languages.  
- Thorough understanding of digital communication and signal processing.  
- CDMA or OFDM background is a plus. |
|                                         | 2. Co-work with ASIC designers to define architecture.                         |                                                             |                                                                                                                                               |
|                                         | 3. Implementing and verifying digital baseband receiver algorithm.              |                                                             |                                                                                                                                               |
| Senior Software Engineer                | This position will be responsible for developing device drivers, BSP and Linux tweaks for a wireless communication module on customized NIOS-II based hardware platform. Ideal candidate will have extensive Linux internals knowledge and experience in developing device drivers on off-the-shelf or custom operating systems. | Education Requirement: MS degree or above in EE related fields | Work Experience: 5 years+  
Some hardware knowledge is a plus; Linux kernel experience is a big plus.  
Experience and Skill Requirements:  
- 5 + years of experience with Linux programming  
- Extensive Device driver and BSP experience, especially with any wireless systems (802.11, 3G, etc)  
- Knowledge of embedded O/S bootcode, diagnostics and administration (GRUB/U-Boot, initd, etc)  
- Extensive Experience in C programming for embedded systems |

Document for ASIC design engineers and to co-work with ASIC designers to define architecture, write C-model for verification after the algorithms are complete. Experience with the digital baseband to RF interface, AGC, and AFC is a plus. The candidate is expected to be fluent in both Matlab and C programming languages.
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| Administrative staff | 1. general accounting matters  
2. Assist administration and other general service matters  
3. Purchase order and inventory management | Bachelor degree required       | 1. Over 2 years accounting experience is preferred  
2. at least 4 years working experience  
3. English: good at listening and speaking, fluent at reading and writing. |

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