

南港IC設計育成中心

專晶半導體科技股份有限公司

公司介紹

公司地址：台北市南港區園區街3-2號9樓903室

公司網址：<http://www.icartisusa.com/>

公司簡介、經營理念、遠景

專晶半導體科技股份有限公司是首家獲經濟部ITRI核准進駐南港SoC設計園區-育成中心專業開發矽智慧財產(SIP, Silicon Intellectual Property)，並提供系統單晶片(SOC)設計代工服務的公司。主要業務包括IP授權、SOC設計服務及SOC設計轉成品服務(Turnkey Service)；並提供MPW(Multi-Project Wafer)等服務。本公司致力於提供全方位的整合服務(one-stop)、全方位SOC設計代工的專業服務，協助客戶在最短時間內完成及最有效的成本從設計概念到產品上市，縮短客戶的產品開發時間。

我們擁有：

- A. IP組合：包括處理器、多媒體、混合訊號、記憶體、通訊介面等領域；
- B. 設計流程方面：已建置 $0.35\mu\text{m}$ 、 $0.25\mu\text{m}$ 、 $0.18\mu\text{m}$ 、 $0.15\mu\text{m}$ 、 $0.13\mu\text{m}$ 、90奈米及65奈米等深次微米設計流程。
- C. 結合全球封裝與測試合作夥伴，提供了全方位的整合服務全球客戶，從設計到量產的完整SOC解決方案SOC設計與量產的服務
- D. IP組合、SOC設計及量產的專業整合代工服務，協助客戶以最有效率的方法從設計概念的產生到產品上市將可縮短客戶的產品開發時間。
- E. 核心團隊成員來於美國矽谷半導體產業深耕於類比科技，其中包括多位極具聲望之留美博碩士。

主要商品與服務

IP授權及OUT-SOURCING。

SOC設計服務。

SOC TURNKEY設計、製造、測試。

MPW服務。

特殊應用積體電路技術服務

矽智財與設計平台

系統單晶片整合服務

福利制度

1. 享勞、健、團保。
2. 三節獎金(端午、中秋、春節)。
3. 婚、喪、住院、生育、生日....等補助金。
4. 重視人才的培育及發展，出國受訓機會多。
5. 舒適安全及人性化的工作環境與組織氣氛。
6. 加班用餐補助。
7. 週休二日，上班五天。

職缺列表

職務名稱	資格條件	經歷	學歷	工作地區
Sr. Product Manager	This position requires a Bachelors Degree in Engineering (materials science, mechanical engineering, microelectronics or similar) and		大學/ 碩士	南港

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	<p>10+ years of experience in the semiconductor industry or a Masters Degree in Engineering and 8+ years experience. This position also requires project management experience, knowledge of surface mount technologies and final package from factors. The successful candidate must be able to work independently and have experience with design and manufacturing systems used in packages.</p> <p>Knowledge of material and package characterization data analysis, test methods and qualification procedures is desired. Technical presentations for customers required. The candidate should have excellent written and verbal communication skills in English.</p> <p>This position requires 5% domestic and 10% international travel.</p>			
Analog Circuit Design Engineer	<p>- MS and 3-9 years of experience CMOS analog design/RF integrated circuit design (willing to take recent graduate with solid education and experience) - Requires strong background in circuit analysis and deep understanding of chip level issues - Experience in Cadence design environment - Experience in relevant analog circuit design</p>		大學/ 碩士	南港
IC Layout Engineer	<p>1. BS or MS degree in Electrical Engineering or Computer Science 2. Experience/exposure to IC physical design especially in 0.18um/0.13um and below technology. 3. Educational background/experience with logical and physical synthesis, VLSI design, static timing analysis methods and tools, place and route. 4. Experience with ASIC design in one or multiple of the following EDA tools. Synopsys Astro, Cadence SOCE, Magma. 5. Solid communication skills and ability to work in team environment. 6. Willing to take initiative, self-driven personality.</p>		大學	
IC Layout Manager / IC Layout 經理(台灣)	<p>BSEE (MSEE preferred) -4 years experience in IC Design, Physical Design, Physical Verification, and Tapeout 2 years experience as a Manager and Team Leader -Demonstrated ability managing projects with a high degree of organization and priority -Strong communication skills, high level of intelligence, self-motivation, and aggressiveness -Fluent in English and Chinese-需出差美國及大陸</p>		大學	
analog layout customers support engineer	<p>An analog layout customers support engineer able to coordinate with analog IC designer and layout partners, ensuring robust, consistent and successful delivery of analog layout projects. Knowledge of multiple key analog layout areas and their implementation from top level floorplanning down to complex block level layouts. Hands-on experience conducting analog circuit layout analysis and recommending appropriate solutions. A basic understanding of IC</p>		大學	

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	design technology and process/methodology in IC analog design layout solutions. The ability to exchange and communicate information with management, customers and peers are needed. The end-in-mind is customer satisfaction, all job responsibilities are geared towards providing superior customer support. Applicable experience includes working product knowledge of Cadence Virtuoso layout tool, Mentor Graphic Calibre, Springsoft Laker series or in-house tools.			
IC physical design customers support engineer	An physical implementation customers support engineer will have demonstrated hands-on experience and expertise with physical design tools (Design Compiler, Primetime, Astro, Encounter, or Blast) or equivalent tools, flows and methodologies required to execute a physical design project. Solid verbal and written English communication skills, customer interfacing skills, and business and business savvy are necessary. The end-in-mind is customer satisfaction, all job responsibilities are geared towards providing superior customer support		大學	

履歷下載



將履歷下載後, 填寫完後請將履歷寄至: stc_nk@itri.org.tw

服務電話: 02-2655-7155#320 吳小姐。謝謝~